SECTION 6 SPECIFICATIONS

General

POWER SUPPLY

Voitage

205-255 or 105-127 Volts

Line Frequency

50Hz ± 2%, 60Hz ± 2%,

400Hz ± 2%

Consumption

: Approximately 30VA

Fuses

: 160mA or 500mA anti-

surge (depends on voltage)

MECHANICAL

Dimensions

: Height = 89mm, Width =

455mm, Depth = 420mm

Weight

: 10 kg.

AUTORANGE

Range Up

200% of nominal range

Range Down

: 18.8% of nominal range

SAFETY

The 1065 has been designed to meet BSI 4743,

IEC 348, and UL 1244 specifications.

MAXIMUM INPUTS

See Tables 2.1 & 2.2

CLIMATE CONDITIONS

Operating Temperature : 0°C to +50°C (except where

specified)

[1], Storage Temperature

: -40°C to +70°C

Maximum Relative

Humidity

: 75% @ 40°C

Warm-up Time

Two hours to meet all speci-

fications

OPERATING INDICATIONS

Scale length

: Normal

5½ digits

i.e. 199,999 4½ digits Superfast

i.e. 19,999

1065A 6½ digits

(DCV & kΩ) i.e. 1,999,999

Overload

Err OL displayed

Indication

: Symbols lit on display and

illuminated keys

DIGITAL ERROR

Error read-out

<1% of displayed error

DC VOLTAGE

Full range Count (FR): ± 100,000

Full Scale Count (FS): ± 199,999 on all ranges

except 1000V range

Superfast Mode Full Scale Count: 19,999 on all

ranges except 1000V range.

ACCURACY

24 HOURS (23°C ± 1°C)

0.1V range: ± 20ppm of reading ± 3 digits 1 and 10V ranges: ± 15ppm of reading ± 1 digit 100 and 1000V ranges: ± 30ppm of reading ± 1 digit

90 DAYS (23°C ± 5°C)

0.1V range: ± 60ppm of reading ± 4 digits 1 and 10V ranges: ± 40ppm of reading ± 1 digit 100 and 1000V ranges: ± 70ppm of reading ± 1 digit 1 YEAR (23°C ± 5°C)

0.1V range; \pm 80ppm of reading \pm 4 digits 1 and 10V ranges: ± 60ppm of reading ± 1 digit 100 and 1000V ranges: \pm 100ppm of reading \pm 1 digit

Superfast Mode (all ranges): ± above ppm of reading ± 2 digits

TEMPERATURE COEFFICIENT: (10°C to 35°C)

1/10th of 90 DAY specification ± 0.2 µV/°C

READ RATE

Normal Mode

All DC ranges: 3/second (internal trigger) with full scale input

30/35 per second (external trigger) with full

range input at 50/60Hz.

Superfast Mode

All ranges: 200/220 per second (external trigger)

with full range input at 50/60Hz.

SETTLING TIME (to 10ppm of step size;[1]

Filter out:<5mS Filter in:< 350mS

SERIES MODE REJECTION

Filter out: 66dB@line frequency Filter in: 100dB@line frequency

COMMON MODE REJECTION

1kΩ source unbalance

>140dB at DC

> 80dB + series mode at 1Hz to 60Hz

AUTORANGE SPEED (No filter)

Typically 100mS per range between top and bottom ranges.

INPUT RESISTANCE

0.1 to 10 Volt ranges (< 20 volts):>10,000 MΩ 100 and 1000 Volt ranges: $10M\Omega \pm 0.1\%$.

INPUT ZERO STABILITY (1 year)

<50pA,<5 \(\mathcal{V} \).

RESISTANCE

Full Range Count: 100,000 Full Scale Count: 199.999

Superfast Mode Full Scale Count: 19,999

ACCURACY

24 HOURS (23° ± 1°C)

100Ωrange: ± 25ppm of reading ± 4 digits 1kΩ, 10kΩ ranges: ± 15ppm of reading ± 1 digit \pm 20ppm of reading \pm 1 digit 100k() range: 1000k() range: ± 100ppm of reading ± 1 digit 10M Ωrange: ± 200ppm of reading ± 1 digit

90 DAYS (23°C ± 5°C)

100 Drange: ± 60ppm of reading ± 4 digits $1k\Omega$, $10k\Omega$ ranges: ± 40ppm of reading ± 1 digit \pm 50ppm of reading \pm 1 digit 100kΩ range: 1000kΩ range: ± 150ppm of reading ± 1 digit 10MΩ range: ± 400ppm of reading ± 1 digit

1 YEAR (23°C ± 5°C)

100 Ω range: ± 80ppm of reading ± 4 digits 1kΩ, 10kΩ ranges: ± 60ppm of reading ± 1 digit ± 70ppm of reading ± 1 digit 100kfl range: 1000kΩrange: ± 250ppm of reading ± 1 digit 10MΩrange: ± 600ppm of reading ± 1 digit

Superfast Mode: As DC Volts.

TEMPERATURE COEFFICIENT: (10°C to 35°C)

1/10th of 90 DAY specification \pm 600 $\mu\Omega$ /°C

READ RATE

Normal Mode

All ranges: As DC Volts. Superfast Mode: As DC Volts.

True 4-wire with active guard (automatic 2-wire

on the front panel).

Measurement technique is independent of the internal reference voltage.

OPEN CIRCUIT VOLTAGE

< 10 volts on all ranges

LEAD RESISTANCE

Up to 1011 may be tolerated in any or all leads on any range. (Rejection of lead resistance is 80d8 on any range).

RESPONSE TIME

Depends on external capacitance and guarding/shielding techniques used.

Generally up to 10kn response as DC Volts.

Higher resistances take longer to settle.

OHMS GUARD may be used to guard out stray capacitance

CURRENT THROUGH UNKNOWN (± 1%)

0.1kΩ ranges: 1mA 1kΩ range: 1mA 10k() range: 100 µA 100k Ω range: 10 μ A 1000k Ω range: 1 μ A 10MΩ range: 100nA

OHMS GUARD (rear input only)

Drive Capability: I+ or I- to OHMS GUARD.

500 Ω minimum (up to 10 Ω lead resistance) Guarding Accuracy: See Section 2 - 'Resistance measurement'.

INPUT ZERO STABILITY (1 year)

< 10m Ω

AC VOLTAGE (TRUE RMS)

Full Range Count: 100,000

Full Scale Count: 199,999 on all ranges except 1000V range.

ACCURACY (Signals < 107 Volt Hz

> 1.0% Full Scale

24 HOURS (23°C ± 1°C)

1V to 1000V ranges:

90 DAYS (23°C ± 5°C) 1V to 1000V ranges:

1 YEAR (230 ± 50C) 1V to 1000V ranges: 45Hz^[1] to 30kHz

± 0.04% of reading ± 50 digits

± 0.06% of reading ± 50 digits

 \pm 0.1% of reading \pm 50 digits

30kHz to 100kHz [2]

100kHz to 1MHz [2]

± 0.2% of reading ± 200 digits

± 5% of reading ± 2000 digits

± 0.4% of reading ± 200 digits

± 6% of reading ± 2000 digits

 \pm 0.6% of reading \pm 200 digits

± 7% of reading ± 2000 digits

DC coupled AC total accuracy specifications are x 1.5

LF ACCURACY add

Filter out, at line frequency: ± 0.6% of reading Filter in, 10Hz: ± 2.0% of reading

CREST FACTOR

5:1 typically, at full range

TEMPERATURE COEFFICIENT (10-35°C)

<1/10th of 90 DAY specification/°C

COMMON MODE REJECTION

1KΩ unbalanced > 90dB@DC - 60Hz

READ RATE (as DC voltage)

INPUT IMPEDANCE

1MII shunted by 150pF

CONVERSION TYPE

True r.m.s. AC coupled (measures AC component with up to 1000V DC bias on any range) or DC coupled

(measures $\sqrt{AC^2 + DC^2}$)

SETTLING TIME (DC coupled)

(i) To 0.1% of step size Filter out < 150mS

Filter in < 500mS

(iii) From DC bias input (AC coupled) or severe overload: Depends on change of DC bias (Time constant 2.2 seconds)

- [1] Read 360Hz instead of 45Hz if 'Filter' not selected.
- [2] Error readout invalid above 30kHz.

Standard internal delays

Between the recipt of any trigger pulse and the commencement of a measured cycle, an internal time delay is introduced.

This permits the application of the signal to the input terminals to be coincident with the trigger and ensures that the input circuitry has settled before the commencement of the reading cycle.

The standard internal delays differ for each range and function in order to ensure maximum read-rate and adequate settling. The delays are shown in the following table.

Additional to all the delays shown is 25mS when changing range between 10V and 100V ranges and 100mS before the first reading following a function change.

Function	Range	FILTER OUT (ms)	FILTER IN (ms)
DCV	all	5	500
ACV DC + ACV	all	225	750
kΩ	100Ω $-100 \mathrm{k}\Omega$ 100Ω 100Ω	5 15 150	500 600 1250